

An EESA News Letter

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JULY-DECEMBER 2020

Rajarambapu Institute of Technology, Rajaramnagar

(An Autonomous Institute)

Department of Electronics and Telecommunication Engineering

E-BUZZ 2K20

EESA Committee

Dr.M.S.Patil

(H.O.D ETC DEPT.)

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(Faculty Coordinator)

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(Treasurer)

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(Event Coordinator)

Miss. Prajakta Shinde

(PG Representative)

E-BUZZ

It is our pleasure to publish before you our newsletter e-buzz 2k20. E-buzz focuses on the activities in which faculty members and students of etc. dept. have shown their interest in higher education as well as research and placements. This newsletter also shares various activities conducted by EESA along with achievements of faculty members and students in different fields.

EESA (Electronics and Telecommunication Engineering Student Association) is a departmental student association. It is an initiative taken by students to do extra curricular activities. Various activities are conducted by essay for overall student development. It helps student to develop skills like communication, aptitude, presentation skills, general awareness along with technical knowledge.

All these factors eventually contribute in becoming a complete engineer, which basically is the motto of our college. It helps our students in professional world both on technical and personal levels.

EESA constantly strives for student development by offering its members to enhance their abilities and develop new ones by organizing events, workshops, industrial visits etc.

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Teacher's Day

Teacher's day was celebrated with great enthusiasm under the EESA committee. Teachers were honored for their work with a token of love from their beloved students



Indian tradition respects the teacher with high esteem. There is no denying the fact that the role of a teacher in a student's life is very crucial right from the time when a child joins a day care centre to the stage when he/she takes a step forward towards establishing a professional career. The early growing years in a child's life are the most crucial and lay the foundation for developing their nature, personality and the individual self. It has been rightly said that



'children are the future of any country

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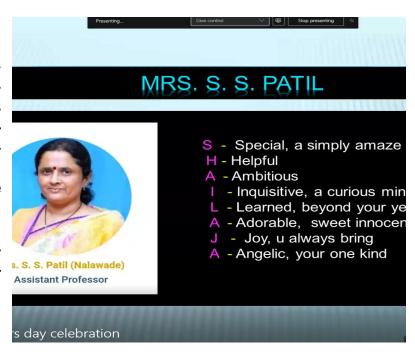
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Some speeches were delivered by the students to express their gratitude towards teachers. Teachers were felicitated on this wonderful occasion of teachers day.

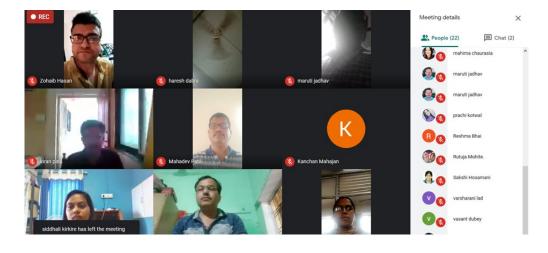
A good teacher can inspire hope, ignite the imagination, and instill a love of learning. Teaching is a very noble profession that shapes the character, caliber, and future of an individual





On the occasion of teachers day celebration EESA inauguration was held. Prof. V.S.Patil EESA coordinator has shared her views about EESA and also explained Agenda of activities conducted under EESA though out the year. Members of EESA committee were felicitated by teachers.

Workshop on Two days Virtual training program on Design and simulation of Antenna in HFSS



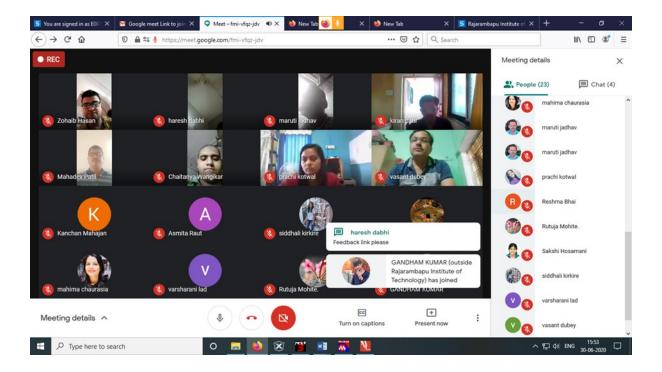
Department of Electronics and Telecommunication Engineering has organized a Two days Virtual training program on Design and simulation of Antenna in HFSS. The workshop was organized under the banner of RIT center for Teaching Learning (RIT-CTL). It was held in an online mode on 29th and 30th June 2020 from 10am to 4.30 pm.

Total Number of participants- 39

Students and Faculty members from the different parts of the country have participated and learned to design and simulate antenna using software Ansys HFSS. The necessary software was shared to participants well in advance for installation, the online meeting was conducted a day before the workshop to guide participants for installation.

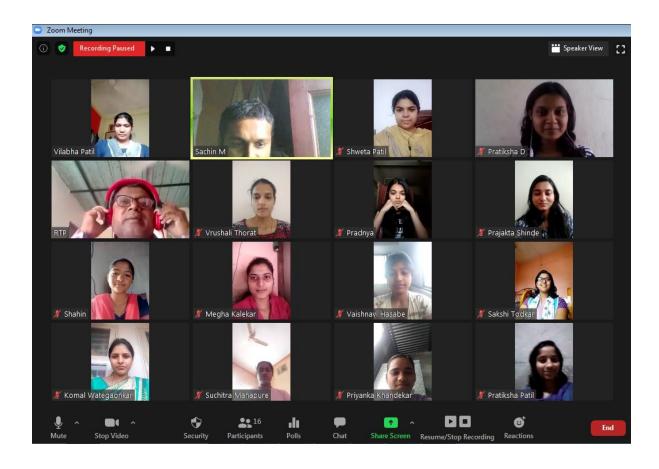
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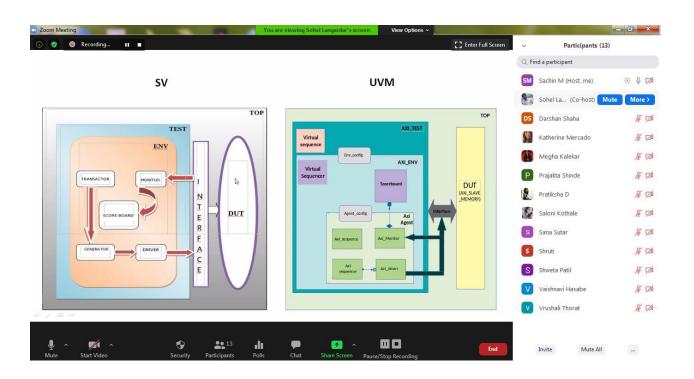
Dr.A.B.Kakade(Dean R&D) delivered the session on fundamentals of Antenna and Prof. M.R.Jadhav conducted the synchronous hands on sessions on the software. The online sessions were conducted using online meeting tool Google Meet. Prof. M.R.Jadhav has coordinated the entire activity under the guidance of Dr. M.S.Patil, Head E&TC dept. Special thanks to Dr.S.K.Patil, Dean Academics and Hon. Director Dr. Sushma Kulkarni for their valuable guidance, support and encouragement.

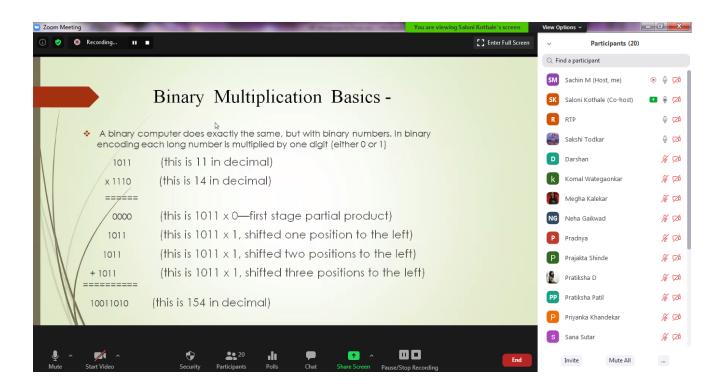


Virtual Internship Program (Collaboration with Global Engineering Dean's Council India Chapter) on VLSI FRONT END DESIGN AND VERIFICATION

Design And Verification" from November 22nd June to 22nd July,2020. Total 23 participants were attended this course from Spain, USA, India (Goa, Maharashtra states).







Two days webinar on "Research Paper Writing and Intellectual Property Rights"



Department has organized Two days webinar on "Research Paper Writing and Intellectual Property Rights" from 18th to 19th May 2020.

As a part of socio-academic responsibility in this tough time of lockdown we from E&TC department under the umbrella of RIT, Rajaramnagar had organized 2 days webinars for faculty of engineering, industry professionals, graduates and post graduates on "Research Paper Writing and Intellectual Property Rights" in with IEEE Bombay Section, IETE student chapter.

We had around 1000+ registrations from all over India for this webinar.

Student's Achievements Placement 20-21

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