

5.1.3 The following Capacity Development and Skill Enhancement activities are organised for improving students' capabilities

1. Soft skills, 2. Language and communication skills, 3. Life skills (Yoga, physical fitness, health and hygiene)
4. Awareness of trends in technology

<b>Name of the Capacity Development and Skill Enhancement programme</b>	<b>Year of implementation</b>	<b>Number of students enrolled</b>	<b>Name of the agencies/consultants involved with contact details, if any</b>
Guest Lecture on Path to Overseas Higher Education and Opportunities in Electronics field	2022-23	75	Mr. Ashish Patil, Sr. Engineer, Qualcomm San-Diego, California, USA (Contact No: +1248-9976117)
Expert Lecture on Electronic Devices and Industrial Applications	2022-23	61	Mr. Rushikesh Nikam Technology Analyst, System Engineer, Infosys Pune (Contact No: 8425907400)
Expert Lecture on CMOS memory design	2022-23	52	Mr. Sushant Gawade Assistant Engineer VLSI design Silicon Interfaces Pvt. Ltd., Mumbai (Contact No: 9870848850)
Expert Lecture on Enhancing Soft Skills	2022-23	90	Mrs. Durga Deshmukh Proprietor at Rudransh System Solutions Belgaum (Contact No: 9036087990)
Expert Lecture on Generative AI, Chat GPT, Large language models and diffusion based models	2022-23	133	Mr. Sourabh Kulkarni Research Scientist Meta Facebook (Contact No: +17179929292)
Expert Lecture on "Embedded System Design using IOT"	2022-23	63	Dr. Y. Srinivasa Rao Bharatiya Vidya Bhavan's Sardar Patel Institute of Technology Mumbai, India
Expert Lecture on Overview of GPU and its Applications in Data Centers	2022-23	125	Mr. Karan Ahuja Senior Silicon Product Engineer at NVIDEA, California, US (Contact No: +17327999385)
Expert Lecture on Software Testing	2022-23	55	Mr. Prashant Khade Asst. Prof. Adarash Institute of Technology, Vita (Contact No: 7507161228)
Expert Lecture on Cloud Computing	2022-23	58	Mr. Sachin Jalwadi Programmer Analyst at Cognizant (Contact No: 9325475044)
Guest Lecture on Role of Engineers in Social work	2022-23	62	Ms. Yogeshwari Patil Social Worker NGO, Quality Education Support Trust (QUEST) Palghar, Raigad (Contact No: 9545044483)
One day workshop on Structure Query Language (SQL)	2022-23	19	Prof. P.P. More (Contact: +91 7276868180) Prof. Ms. A.A. Pujari (Contact: +91 7387920772)

One day workshop on Let's Simulate Stepping into the World of Simulation	2022-23	106	Under Lead College Scheme of Shivaji University, Kolhapur Ms. S.R. Watharkar (Contact: 7709889174)
Expert Lecture on Intellectual Property Awareness	2022-23	56	Mrs. Saylee Sandeep Bidwai Head-Entrepreneurship Development Cell, MITAoE, Alandi (Contact: 91-9423351251)
Line Follower Robot competition ROBOCAD MICROMOUSE	2022-23	30	Under Lead College Scheme of Shivaji University, Kolhapur
Two-week Internship Program VLSI FRONT END DESIGN	2022-23	24	Mr. Sachin Magdum, Sourabh Patil, Abhishek Borote, FPGA Design Engineer, Linked Loops, Pune (Contact: 9535478999)



  
 HEAD,  
 Department of Electronics & Telecomm. Engg.  
 R. I. T. Rajaramnagar (Sakhraig)  
 415414





Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
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Guest Lecture  
On  
**Path to Overseas Higher Education and Opportunities in  
Electronics field**

By  
**Mr. Ashish Patil, Sr. Engineer, Qualcomm**  
**San-Diego, California, USA**

*24<sup>th</sup> November 2022*  
*1.00PM-3.00PM*

Organised under  
**Electronics & Telecommunication Engineering Students  
Association (ETESA)**



Faculty Coordinator  
**Prof. S.R.Watharkar**  
Email: [snehal.watharkar@ritindia.edu](mailto:snehal.watharkar@ritindia.edu)  
Contact: +91 7709889174

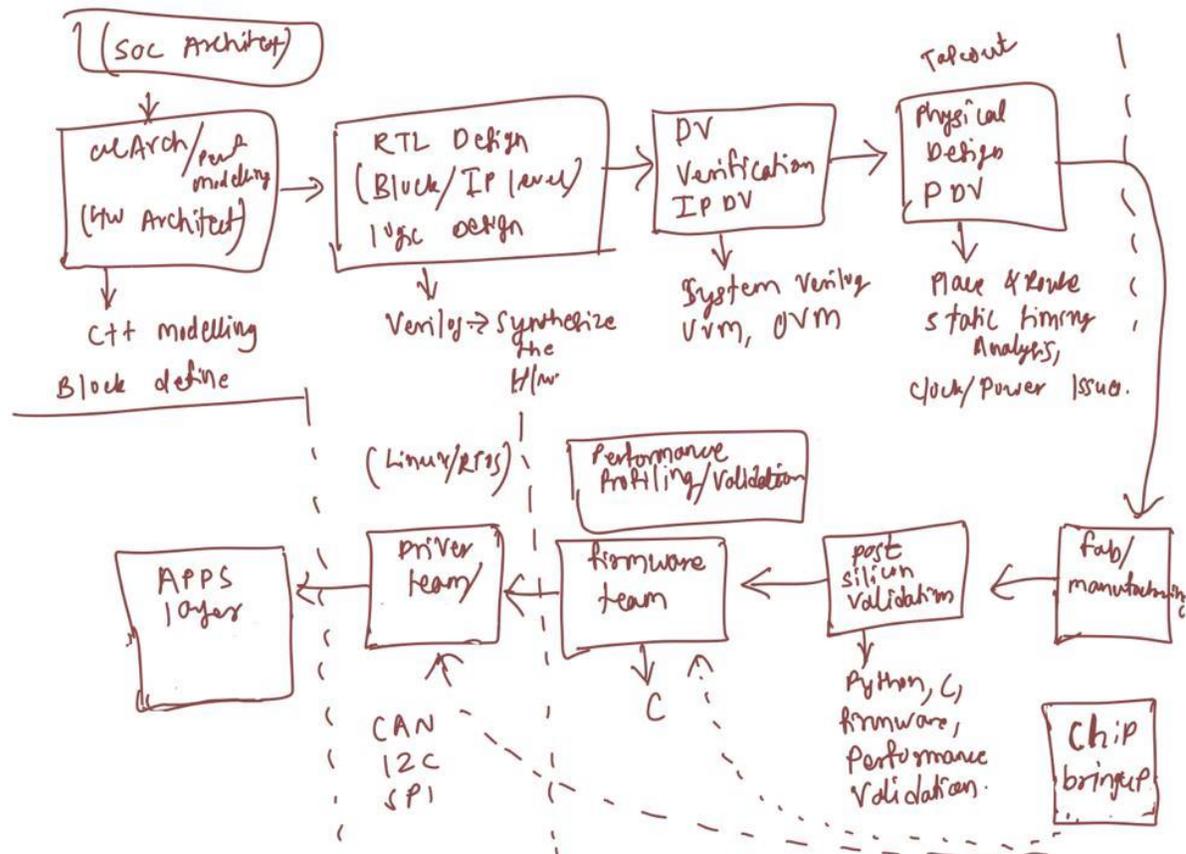


## About the Guest Lecture

The main focus of this lecture was to aware the students about overseas opportunities for an Electronics & Telecommunication engineer and recent trends in Electronics Industry. This lecture was organized to motivate the students to work in a core electronics industry and inspire them for the higher education/ masters abroad.

The lecture was held in two sessions. In first session, he guided the students about GRE. He discussed about the GRE and Toefl exam preparation, application and admission process in USA. He also discussed about how to apply and get internship offers in USA. He focused on the subjects or Major tracks can be taken during Masters to get full time job

In second session, he started with career path opportunities in which he covered basic introduction about different domains in electronics like digital, analog, embedded systems. He briefed about opportunities in digital electronics and embedded domain with the help of pipelined diagram of the basic Chip Design and Manufacturing, right from concept to market and skills required to work in each of those domains.





## About the Resource Person

The resource person was Mr. Ashish Patil. He is senior engineer at Qualcomm, San-Diego, California, USA. He has completed his masters in Electrical & Computer Engineering from Portland State University, Oregon, USA in 2017. He completed his bachelor's degree in Electronics Engineering from Walchand College of Engineering, Sangli in 2012.

He has 8 years of industrial experience in core electronics domain. He worked in Tata Elxsi in 2012-2014. He did one year internship in Hitachi in 2016-17. He worked in Valeo from 2017 to 2020. Currently he is working in Qualcom, USA from 2020 as a senior engineer.

**rit**  
RAJARAMBAPU INSTITUTE OF TECHNOLOGY

**K.E. SOCIETY'S  
RAJARAMBAPU INSTITUTE OF TECHNOLOGY,  
ISLAMPUR**

**E.T.E.S.A**

**Department of Electronics and Telecommunication**

**MR. ASHISH PATIL**  
**SENIOR ENGINEER AT QUALCOMM**  
**SAN-DIEGO, CALIFORNIA, USA**

**Achievements**

- Runner-up  
Computer Architecture Design  
Competition  
Portland State University
- 1st prize winner at u-tron  
national level technical convention  
vision 2011  
Walchand College of Engineering  
Sangli

**Thursday**  
**24 Nov**

**Education**

- B.Tech in Electronics  
2008 - 2012  
Walchand College of Engineering, Sangli
- M.S in Electrical and Computer Engineering  
2015 - 2017  
Portland State University, Oregon, USA

**Industrial experience**

- Qualcomm 2020 - present
- Valeo 2017-2020
- Hitachi - internship 2016 - 2017
- Tata elxsi 2012 - 2014

## No. of Participants

- Final Year B.Tech ETC: 75



## Glimpses of the Guest Lecture





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*M. S. Athi*  
HEAD,  
Department of Electronics & Telecomm. Engg.,  
R. I. T. Rajaramnagar (Sakhrisra)  
415414

## Rajarambapu Institute of Technology, Rajaramnagar Electronics and Telecommunication Engineering

### Report of Expert Lecture by Rushikesh Nikam



**REALConnect**  
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LEVERAGING THE POWER OF ALUMNI

Department of Electronics and  
Telecommunication Engineering  
Rajarambapu Institute of Technology,  
Rajaramnagar

**Electronic Devices and  
Industrial Applications**

**Tuesday,**  
**10 January, 2023**

11 A.M-12 P.M

Venue: ETC  
Department

**Rushikesh Nikam**

#### 1. Expert short information:

Technology Analyst, System Engineer, Infosys Pune.

Linkdin Profile Link:

<https://www.linkedin.com/in/hrishi16>

#### 2. Course details

Title of program: “Electronic Devices and Industrial Applications”

**Objectives:**

- To provide insights into the significance of electronic devices in industrial applications.
- To explore the diverse range of electronic devices used in different industrial sectors.
- To inspire and motivate students to pursue careers in the field of electronic devices and industrial applications.

**Benefit in terms of learning/Skill/Knowledge obtained:**

- Understand the fundamental principles and concepts behind electronic devices and their applications in various industries.
- Gain knowledge about the different types of electronic devices used in industrial settings, such as sensors, actuators, control systems, and communication devices.
- Recognize the potential career opportunities in the field of electronic devices and industrial applications and the skills required to excel in this domain.

➤ **No. of students attended: 61**

➤ **No. of faculties attended: 03**

**3. Short report:**

The expert lecture on "Electronic Devices and Industrial Applications" by Rushikesh Nikam provided valuable insights into the significance of electronic devices in various industries. The lecture served as a platform for students to gain knowledge about different types of electronic devices used in industrial settings and understand the potential career opportunities available in this field. The students left the lecture inspired and motivated to explore the exciting realm of electronic devices and their applications in industrial sectors.

4. Photographs:



HEAD,  
Department of Electronics & Telecomm. Engg.,  
R.L.T. Rajaramnagar (Sakhras)  
415414



Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
**(An Autonomous Institute)**

**Dept. of Electronics & Telecommunication Engineering**

**Expert Lecture Details**

**Name of Expert: Sushant Gawade**

**Designation: Assistant Engineer VLSI design**

**Company Name: Silicon Interfaces Pvt. Ltd., Mumbai**

Topic: **CMOS memory design**

Class: Third Year B. Tech

Course: CMOS VLSI Design

Date of conduction: 8<sup>th</sup> Jan. 2023

**Objectives of Lecture:**

- Explain basic principle of memory operations
- Design different types of memory
- Compare different memory designs

**Points Covered:**

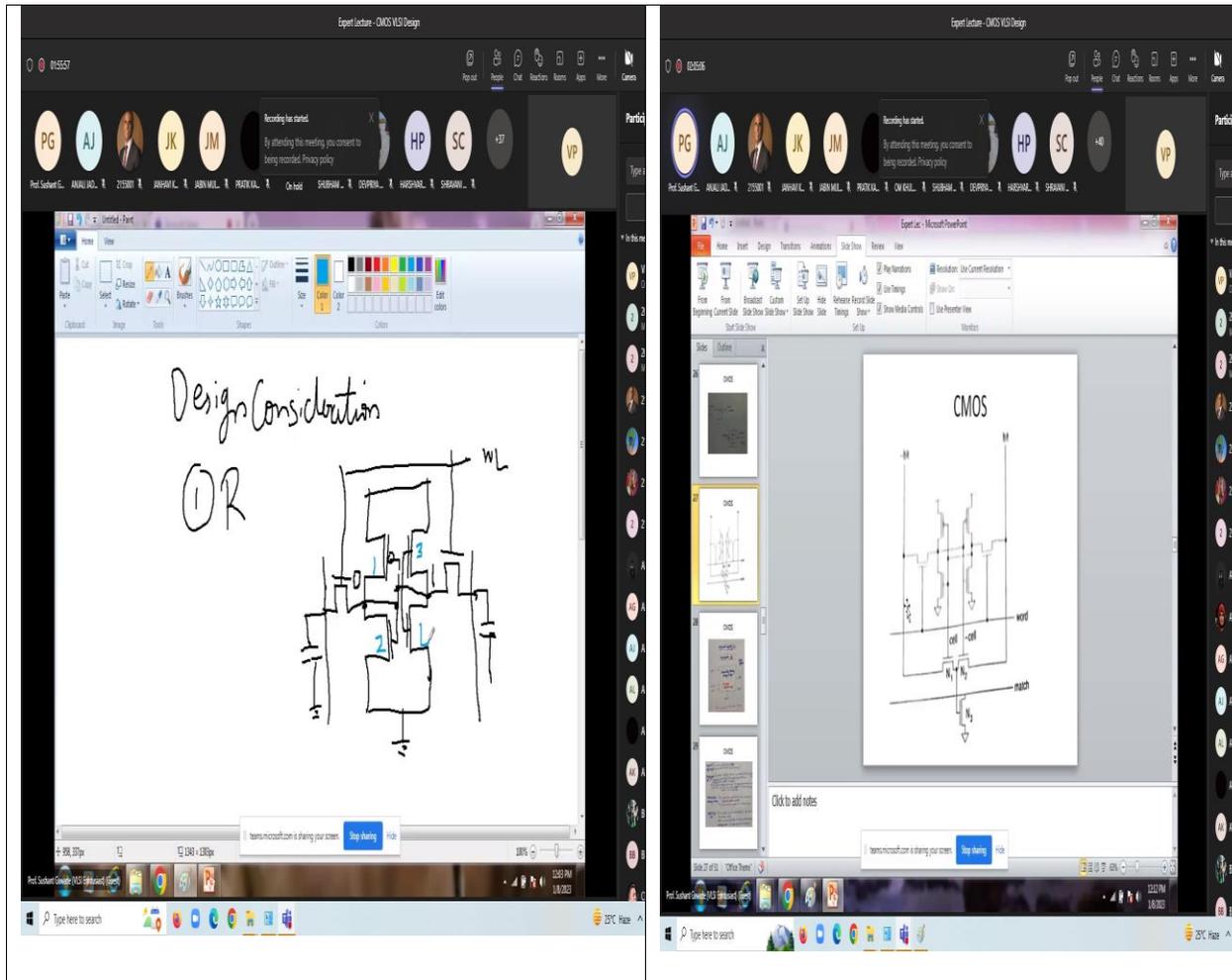
- Introduction to memory, SRAM Memory design and operations , DRAM Memory design and operations and CAM Memory design and operations

**Number of Participants: 52**



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**Screenshots of Lecture:**





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Alumni Industry Expert Lecture

On

**Enhancing Soft Skills**

*By*

**Mrs. Durga Deshmukh-Jagtap**

**Under the aegis of**

**REALConnect**

**RIT ETC Alumni Connect**

A series of expert lectures for leveraging the power of alumni

**17<sup>th</sup> March 2023**

Organized by

**Electronics & Telecommunication Engineering**

Faculty Coordinator

**Dr. Mahesh Kumbhar**

Email: Mahesh.kumbhar@ritindia.edu

Contact: +91 9970700741



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### About the Industry Expert Lecture:

Department of Electronics and Telecommunication Engineering from Rajarambapu Institute of Technology, Islampur have organized expert session by Mrs. Durga Deshmukh-Jagtap, who is proprietor at Rudransh System Solutions Belgaum and alumni of 2007 batch on Enhancing Soft Skills - **17<sup>th</sup> March 2023**. Soft Skills are personal attributes that enhance an individual's interactions, job performance and career prospects, it is the character traits and interpersonal skills that characterizes a person's relationships with other people. In simple terms, Soft Skills have more to do with who we are than what we know. In the workplace, soft skills are considered a complement to hard skills, which refer to a person's knowledge and occupational skills. The Soft Skills required would be business ethics, conflict management, team building, coaching and mentoring, and project and time management. As a part of soft skills training, students imparted in-depth coverage on the topics such as Group Discussions, Interpersonal skills, Interview skills, Grooming, Body Language, Resume Writing.

Students from second year and third year B.Tech Electronics and Telecommunication Engineering have attended the session. Students will definitely get benefits in their upcoming placement drives and career opportunities.



  
M. J. Jagtap  
HEAD,  
Department of Electronics & Telecomm. Engg.,  
R. I. T. Rajaramnagar (Sakhsaria)  
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Kasegaon Education Society's  
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Alumni Industry expert lecture

On

**Generative AI, Chat GPT, Large language models and diffusion  
based models**

**Under the aegis of**

**REALConnect**

**RIT ETC Alumni Connect**

A series of expert lectures for leveraging the power of alumni

*28<sup>th</sup> March 2023*

Organised by

**Electronics & Telecommunication Engineering**

Faculty Coordinator

**Dr. Mahadev S. Patil**

Email: mahadev.patil@ritindia.edu

Contact: +91 9420796900



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## **About the Industry Expert Lecture**

There are many disruptive technologies, AI is one among them, which will have far reaching impact in all aspects of society - known as generative AI. Two main aspects of generative AI have shown striking results - large language models (AI that can generate text close to human level) and image generation models (AI that can generate images/videos close to real world or human artist levels). The talk will go over what these models are, how they work, show some capabilities, and finally discuss the potentials and risks of these technologies.

## **About Resource Person**

Sourabh is a 2015 graduate from RIT ETC Dept. He is currently working as a Research Scientist in Meta (Facebook), in the domain of Responsibility, Privacy and ML Explainability. He is part of a team that launched an AI Transparency product called 'Why am I seeing this ad', or WAIST for short, in Feb 2023 on facebook. This tool is currently live for ~40% of global facebook users, and explains to them why the facebook ads ML models choose to show you a certain ad. Before his time at Meta, he was a PhD student at UMass Amherst, where he worked on nanoscale circuit design for ML, computer architectures and algorithms for accelerating applications in genomics and epidemiology.

## **Objective of the Expert Lecture**

- To aware the students about generative AI, Large language models and diffusion based models
- To inspire the students to explore the applications of AI and Chat GPT



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## Contents

- Generative AI
- Chat GPT
- Large language models
- Diffusion based models

## Expected Outcomes of the Expert Lecture

After completion of the workshop, students will be able to

- Explain fundamentals of generative AI.
- Describe Large language models and diffusion based models
- Explore Chat GPT

## No. of Participants

- S.Y. B Tech ETC: 52
  - T.Y. B Tech ETC: 38
  - S Y B Tech CSE: 08
  - T.Y. B Tech CSIT: 35
- Total Participants: 133

## Feedback

Sr. No.	Name of Student	PRN	Class	Do you think, this expert lecture has given great insights on conducted topics	Was the resource person knowledgeable?	Does REAL Connect (RIT ETC Alumni Connect) helped you to connect and interact with proud alumni and industry experts?	Overall Feedback	Rate the session on a scale of 5
1	Saniya vahid bahadurwade	2105001	SY - ETC	Yes	Yes	Yes	Useful interaction	5
2	Riya Rajesh Redekar	2105028	SY - ETC	Yes	Yes	Yes	Lecture was helpful	4
3	Janhavi Mohan Kadam	2005009	TY - ETC	Yes	Yes	Yes	It was informative	4
4	Vaishnav Laxman Kurade	2105015	SY - ETC	Yes	Yes	Yes	Good	4
5	Saniya Firojkan Pathan	2255008	SY - ETC	Yes	Yes	Yes	No	5
6	Sakshi Dayanand Kshirsagar	2105018	SY - ETC	Yes	Yes	Yes	he things we learnt in t	4
7	Apurv Dattatray Mahajan	2009014	TY - ETC	Yes	Yes	Yes	.	4
8	Nikita Shivaji Jadhav	2155007	TY - ETC	Yes	Yes	Yes	ession was very infrom	4
9	Sneha Sudhir Salunkhe	2105012	SY - ETC	Yes	Yes	Yes	Session was good	4
10	Shreya Santosh Khochage	2105007	SY - ETC	Yes	Yes	Yes	Good	4
11	Vidya Agatrao Khandekar	2105050	SY - ETC	Yes	Yes	Yes	.	4
12	Tejas Maruti Shinde	2105034	SY - ETC	Yes	Yes	Yes	h increases the knowl	4



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Poster of the Expert lecture

# REALConnect

## RIT ETC Alumni Connect

Leveraging the power of Alumni

**Department of Electronics and Telecommunication Engineering**  
**Rajarambapu Institute of Technology,**  
**Rajaramnagar**

**Topic: Generative AI, Chat GPT, Large language models & diffusion based models**



**Mr. Sourabh Kulkarni**  
Research Scientist, Meta  
(Facebook)

**Tuesday,**  
28th March, 2023

**10.15 A.M-12 P.M**

**MBA Conference hall**



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## Glimpses of the Workshop





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*Mutli*  
HEAD,  
Department of Electronics & Telecomm. Engg.  
R. I. T. Rajaramnagar (Sakhrata)  
415414



Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
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Expert Lecture  
On  
**Embedded System Design using IOT**  
By  
**Dr. Y. Srinivasa Rao**

**Department of Electronics and Telecommunication Engineering**  
**Bharatiya Vidya Bhavan's Sardar Patel Institute of Technology**  
**Mumbai, India**

21<sup>st</sup> April, 2023

Organized by  
**Electronics & Telecommunication Engineering**

Faculty Coordinator

**Prof. R.T. Patil**

Email: [Ramesh.patil@ritindia.edu](mailto:Ramesh.patil@ritindia.edu)

Contact: +91 8275029101



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### **About the Resource Person:**

Y. Srinivasa Rao (Member, IEEE) received the Ph.D. degree in power electronics from the Indian Institute of Technology Bombay, Mumbai, India, in 2010. He has a work experience of more than ten years in the industry. He was the Head of the Department several times and is currently the Vice-Principal as well as the Dean of R&D Department at Sardar Patel Institute of Technology, Mumbai, and His major research publications are in embedded systems, digital power electronics, and communication system.

### **About the Expert Lecture:**

An Expert talk was organized for TY B. Tech. Students in offline mode. The Guest Lecture was given by Dr. Y. Srinivasa Rao on the topic “**Embedded system Design Using IOT**”. The speaker explained how Embedded system design technology is implemented using IOT. The complete workflow and how to implement various projects on embedded design were explained. Latest development in embedded system & design technology in which the software development board like arduino , Raspberry Pie were discussed along with the microcontroller 8051 and PIC Controller . The use of Wireless projects in combination to Microprocessor & how interfacing was discussed with the students. The expert focused on the live session with the interfacing techniques which can be possible with the different processors /controllers. He also explained how embedded systems has revolutionized the world in the field of electronics, and how along with microprocessor various applications and projects can be made possible and can become smart after combining with IOT Technology. He taught students how to develop real time application based good projects using embedded design & technology. An Embedded System is a combination of hardware and software which creates a dedicated computer system that performs specific, predefined tasks. Internet of Things is the ability to transfer data over a network without requiring human to human or human to computer interaction. IoT is network of physical objects or things.

### **No. of Participants**

- Third Year B.Tech ETC: 63



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**Objectives of the Expert Lecture:**

1. To introduce advanced research as well as industry practices in the field of Embedded Systems and IoT.
2. To empower the students with the knowledge, techniques, and methods to develop embedded IoT Products.
3. To enable the participants to acquire knowledge
4. Demonstrate IoT systems using sensors, hardware and open source IoT platforms

**Photographs:**





Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
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Alumni Industry expert lecture  
On  
**Overview of GPU and its Applications in Data Centers**

**Under the aegis of**  
**REALConnect**  
**RIT ETC Alumni Connect**

A series of expert lectures for leveraging the power of alumni

*Monday, 15<sup>th</sup> May 2023*  
*10.10 am to 11.10 am*  
*(Online Mode: M. S. Teams)*

**Organised by**  
**Electronics & Telecommunication Engineering**

Faculty Coordinator  
**Prof. Prabhanjan P. More**  
Email: [Prabhanjan.more@ritindia.edu](mailto:Prabhanjan.more@ritindia.edu)  
Contact: +91 7276868180



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## **About the Industry Expert Lecture**

A graphics processing unit performs rapid mathematical calculations to render graphics, and organizations often employ GPUs to aid in workload acceleration -- specifically to support AI and machine learning. Heavy processing tasks of this type often require multiple GPU chips, each with multiple cores, to do their work.

A GPU performs heavier mathematical calculations than a CPU by employing parallel processing, in which multiple processors take on different parts of the same task. It has its own RAM to store the data it processes. Hence there is an need to understand overview of GPU and its applications in Data Centers. Expert Lecture was delivered by Mr. Karan Ahuja, on 15th May 2023 from 10.10 am onwards and SY and TY students of Electronics and Telecommunication have attended the session through Online Mode (M.S. Teams Platform)

Joining Link : [bit.ly/REALCONNECT\\_004](https://bit.ly/REALCONNECT_004)

## **About Resource Person**

Mr. Karan Ahuja is proud RIT ETC Alumni of 2016 batch, working as Senior Silicon Product Engineer at NVIDIA, California, US. His expertise are in AI-ML domain and in GPU. He has explored himself at a great extent in GPU, its overviews and applications.

## **Objective of the Expert Lecture**

- To aware the students about Graphics Processing Unit (GPU)
- To inspire the students to explore the applications GPU in Data Centers

## **Contents**

- History of Graphics Processing



- CPU vs GPU
- GPU Overview
- Integrated vs Dedicated Graphics
- GPU Architecture
- Applications in Data Centers
- Chat GPT

## Expected Outcomes of the Expert Lecture

After completion of the expert lecture, students will be able to

- Explain fundamentals of Graphics Processing Unit.
- Describe GPU architecture and significance of Integrated and dedicated Graphics
- Explore applications of GPU in Data Centers and Chat GPT

## No. of Participants

- T.Y. B Tech ETC: 62
- S.Y. B Tech ETC: 63

Total Participants: 125 (Through Online Mode : M.S. Teams Platform)

## Feedback

Sr. No.	Name of Student	Email Address	PRN	Class	Do you think, this expert lecture has given great insights on conducted topics	Was the resource person knowledgeable?	Does REAL Connect (RIT ETC Alumni Connect) helped you to connect and interact with proud alumni and industry experts?	Overall Feedback	Rate the session on a scale of 5	Any other comments
1	Maresh kumbhar	maresh.kumbhar@ritindia.edu	1	Faculty	Yes	Yes	Yes	Great Work	5	
2	Saniya vahid bahadurwade	2105001@ritindia.edu	2E+06	SY - ETC	Yes	Yes	Yes	Session was useful	5	Want more sessions like this
3	Abhishek Sonwalkar	sonwalkarabhishek@gmail.com	2E+06	SY - ETC	Yes	Yes	Yes	Good lecture	3	-
4	Riya Rajesh Redekar	2105028@ritindia.edu	2E+06	SY - ETC	Yes	Yes	Yes	Session was helpful	5	.
5	Saniya Pathan	2255008@ritindia.edu	2E+06	SY - ETC	Yes	Yes	Yes	The session really very nice and get knowledge about Chatgpt and importance of AI	5	Nothing
6	Mrunal Vijaysinh Shinde	2105009@ritindia.edu	2E+06	SY - ETC	Yes	Yes	Yes	The session was really nice.It will be helpful in future . We'll be very excited to attend more sessions like this further.	5	
7	Shubham Raghuvir Bhosal	2105037@ritindia.edu	2E+06	SY - ETC	Yes	Yes	Yes	Good lecture	5	
8	Harshada Dattatray Gurav	2108059@ritindia.edu	2E+06	SY - ETC	Yes	Yes	Yes	It was a informative session.	4	No
9	Aishwarva Sham Shete	2105016@ritindia.edu	2E+06	SY - ETC	Yes	Yes	Yes	This session was really helpful.	5	
10	Aniket Ajit Kamble	2105036@ritindia.edu	2E+06	SY - ETC	Yes	Yes	Yes	Excellent	5	
11	Aniket sid	aniketsid4690@gmail.com	2E+06	SY - ETC	Yes	Yes	Yes	Nice	3	
12	Nalawade Tejaswini Sama	nalawadetejaswini31@gmail.com	2E+06	SY - ETC	Yes	Yes	Yes	Better	4	No
13	Swapnali Dadaso Kadam	kadamswapnali076@gmail.com	2E+06	SY - ETC	Yes	Yes	Yes	Very Good	4	No
14	Roshana Ankush waghmo	waghmoderoshana@gmail.com	2E+06	SY - ETC	Yes	Yes	Yes	Good	4	

## Poster of the Expert lecture



# REALConnect

## RIT ETC Alumni Connect

Leveraging the power of Alumni

Department of Electronics and Telecommunication  
Engineering  
Rajarambapu Institute of Technology,  
Rajaramnagar



**Mr. Karan Ahuja**  
RIT ETC Alumni,  
2016 Batch  
Sr. Silicon Product Engg.  
Nvidia, California, US

Topic : Overview of GPU and its  
Applications in Data Centers



Monday,  
15th May, 2023



10.10 A.M-11.10 A.M



Joining Link:

[bit.ly/REALCONNECT\\_004](https://bit.ly/REALCONNECT_004)



Kasegaon Education Society's  
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58:41

Chat People Raise React View Rooms Apps More Camera Mic Share Leave

GN PD  
GAYATRI NI... PRATHMES...  
VK KP  
VAISHNAVI... KOMAL PA...  
SNEHA MU... RUTUJA M...  
SS RR  
SAKSHI SA... RIYA REDE...  
SM 99+  
SHREYASH... Participants

Meeting chat

Saturday 10:38 am  
REALConnect : RIT ETC  
Alumni Connect : Session on  
"Overview of GPU and its  
applications in Data Centers"  
: by Mr. Karan Ahuja (Alumni  
- 2016 Batch)

Today

10:13 am Meeting started  
10:24 am Recording has  
started

11:02 am  
You can ask questions in  
Chat box also

Feedback Link :

Type a new message

Meeting in "General"

16:06

Take control Pop out Chat People Raise React View Rooms Apps More Camera Mic Share Leave

GN KA RD AS SK VK 99+ PM  
GAYATRI NI... Karan Ahuja RUTUJA D... ANURADH... SHREYASH... VAISHNAVI... Participants

Outline

- History of Graphics Processing
- CPU vs GPU
- GPU overview
- Integrated vs Discrete Graphics
- GPU Architecture
- ChatGPT

Participants

Type a name

Share invite

In this meeting (103) Mute all

PM PRABHANJAN MORE  
Organizer

2 2155005

2 2155008

AK Aastha Kulkarni (Guest)  
Meeting guest

AK ABHISHEK KORE

AP ABHISHEK PATIL

AS Abhishek Sonwa... (Guest)  
Meeting guest

ADHIRAJ MALI

AG ADITYA GADHAVE

Karan Ahuja

Rec... 10:29 AM  
15-05-2023



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**Rajarambapu Institute of Technology, Rajarnamnagar**  
 (An Autonomous Institute)

Meeting controls: 50:36, Take control, Pop out, Chat, People, Raise, React, View, Rooms, Apps, More, Camera, Mic, Share, Leave.

Participants: GN (GAYATRI NI...), KA (Karan Ahuja), RD (RUTUJA D...), SK (SHREYASH...), VK (VAISHNAVI...), KP (KOMAL PA...), 99+, PM.

Meeting chat:

- REALConnect : RIT ETC Alumni Connect : Session on "Overview of GPU and its applications in Data Centers" : by Mr. Karan Ahuja (Alumni - 2016 Batch)
- Today
- 10:13 am Meeting started
- 10:24 am Recording has started
- 11:02 am You can ask questions in Chat box also
- Feedback Link : <https://forms.gle/S3REjfkW3i bDrZZ26>

Slide content:

## Introduction to GPUs

Karan Ahuja, 15 May 2023

Meeting controls: 36:27, Take control, Pop out, Chat, People, Raise, React, View, Rooms, Apps, More, Camera, Mic, Share, Leave.

Participants: GN (GAYATRI NI...), KA (Karan Ahuja), RD (RUTUJA D...), AS (ANURADH...), SK (SHREYASH...), VK (VAISHNAVI...), 99+, PM.

Slide content:

## System Architecture Snapshot With a GPU

Diagram components and connections:

- GPU Memory (GDDR5, HBM2,...)** and **GPU** are connected to the **I/O Hub (IOH)**.
- GPU Memory** specifications: GDDR5: 100s GB/s, 10s of GB; HBM2: ~1 TB/s, 10s of GB.
- CPU** is connected to **Host Memory (DDR4,...)** via **DDR4 2666 MHz** (128 GB/s, 100s of GB).
- CPU** and **Host Memory** are connected to the **I/O Hub (IOH)** via **QPI/UPI** (12.8 GB/s (QPI), 20.8 GB/s (UPI)).
- I/O Hub (IOH)** is connected to **NVMe** and **Network Interface** via **PCIe** (16-lane PCIe Gen3: 16 GB/s).

Bottom right note: **Lots of moving parts!**

Meeting chat:

In this meeting (125) Mute all

- PM PRABHANJAN MORE Organizer
- 2 2155005
- 2 2155008
- AK Aastha Kulkarni (Guest) Meeting guest
- AK ABHISHEK KORE
- AP ABHISHEK PATIL
- AS Abhishek Sonwa... (Guest) Meeting guest
- ADHIRAJ MALI
- AG ADITYA GADHAVE
- AS Aishwarya Shete (Guest) Meeting guest
- AI Akshay ingavale (Guest) Meeting guest
- AC Anant Chougule (Guest) Meeting guest
- AK Aniket Kamble (Guest) Meeting guest



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Rajarambapu Institute of Technology, Rajarnamnagar  
(An Autonomous Institute)

34:22

Take control Pop out Chat People Raise React View Rooms Apps More

Camera Mic Share Leave

GN KA RD AS SK VK 99+ PM

GAYATRI N.L. Karan Ahuja RUTUJA D... ANURADH... SHREVASH... VAISHNAVI... Participants

## Integrated vs Dedicated Graphics

INTEGRATED GRAPHICS	DEDICATED GRAPHICS
A method used in computers by manipulate graphics by integrating video hardware to the motherboard	A method of handling graphics by using a separate graphics card known as the dedicated graphics card in a computer
Provides a comparatively lower performance	Provides a higher performance
Consumes less power	Consumes more power
Result in a longer battery life	Result in a shorter battery life
Dissipate minimum heat	Consume more heat
Does not use its own memory - it shares resources with the CPU	Uses its own RAM, cooling system, dedicated power regulators and associated components
Not as costly	Highly costly

Karan Ahuja

In this meeting (125) Mute all

- SP SHUBHAM PATIL
- SK SHWETA KUMBHAR
- SNEHA MULLYA
- SUMERU CHOUGULE
- SK Swapnali Kadam (Guest) Meeting guest
- TP Tejal Pachangane (Guest) Meeting guest
- TS Tejas Shinde (Guest) Meeting guest
- TP Tejashree Patil (Guest) Meeting guest
- TB Tejashri Bhale (Guest) Meeting guest
- TN Tejaswini Nalawa... (Guest) Meeting guest
- T Trupti (Guest) Meeting guest
- VK VAISHNAV KURADE
- VK VAISHNAVI KAWARE

25:04

Take control Pop out Chat People Raise React View Rooms Apps More

Camera Mic Share Leave

GN KA RD AS SK VK 99+ PM

GAYATRI N.L. Karan Ahuja RUTUJA D... ANURADH... SHREVASH... VAISHNAVI... Participants

## CPU – Serial Computing

Deliver 1 Pizza at a time

Karan Ahuja

In this meeting (116) Mute all

- SP SHUBHAM PATIL
- SK SHWETA KUMBHAR
- SNEHA MULLYA
- SUMERU CHOUGULE
- SK Swapnali Kadam (Guest) Meeting guest
- TP Tejal Pachangane (Guest) Meeting guest
- TS Tejas Shinde (Guest) Meeting guest
- TP Tejashree Patil (Guest) Meeting guest
- TB Tejashri Bhale (Guest) Meeting guest
- TN Tejaswini Nalawa... (Guest) Meeting guest
- T Trupti (Guest) Meeting guest
- VK VAISHNAV KURADE
- VK VAISHNAVI KAWARE



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23:10

Take control Pop out Chat People Raise React View Rooms Apps More

Camera Mic Share Leave

GN KA RD AS SK VK 99+ PM

GAYATRI NI... Karan Ahuja RUTUJA D... ANURADH... SHREVASH... VAISHNAVI... 117 people are in the meeting

## Working of a Computer

```
graph LR; subgraph Input; direction TB; I1[Keyboard]; I2[Mouse]; I3[Camera/Mic]; I4[Touch pad]; end; subgraph Store_and_Process_Data; direction TB; CPU[CPU]; Memory[Memory]; end; subgraph Output; direction TB; O1[Solved Problem]; O2[Games]; O3[Image, Video or VR]; O4[Control Signals]; end; Input --> Store_and_Process_Data; Store_and_Process_Data --> Output;
```

Data Input

Store & Process Data

Output

Karan Ahuja

In this meeting (117) Mute all

- Meeting guest
- ANURADHA SHINDE
- ANUSHKA LIGADE
- ARNAV KHANAJ
- BHAKTI BADADWAR
- CHAITANYA BIJARAGI
- DEVPRIYA LAD
- DIPTI GAIKWAD
- DISHA GADEKAR
- Gauri (Guest) Meeting guest
- GAYATRI NIKAM
- Harshada Dattat... (Guest) Meeting guest
- HARSHVARDHAN PATIL
- HARSHVARDHAN SAKALE

20:50

Take control Pop out Chat People Raise React View Rooms Apps More

Camera Mic Share Leave

GN KA RD AS SK VK 99+ PM

GAYATRI NI... Karan Ahuja RUTUJA D... ANURADH... SHREVASH... VAISHNAVI... 116 people are in the meeting

## What is a GPU?

- GPU stands for Graphic Processing Unit
  - Often interchangeably used term is graphics card or video card
- Works in a very similar way compared to a CPU – performs calculations at an incredibly fast rate.
  - Mainly takes on calculations related to graphics, so that CPU can handle everything else
- Originally developed for gaming, now GPUs have widespread applications in AI, Deep learning, high-performance computing

Karan Ahuja

Participants ...

Type a name

Share invite

In this meeting (116) Mute all

- PM PRABHANJAN MORE Organizer
- 2 2155005
- 2 2155008
- AK Aastha Kulkarni (Guest) Meeting guest
- AK ABHISHEK KORE
- AP ABHISHEK PATIL
- AS Abhishek Sonwa... (Guest) Meeting guest
- ADHIRAJ MALI
- AG ADITYA GADHAVE
- AS Aishwarya Shete (Guest) Meeting guest
- AB ANIKET BHOKARE



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**Rajarambapu Institute of Technology, Rajaramnagar**  
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19:22

Take control Pop out Chat People Raise React View Rooms Apps More Camera Mic Share Leave

GN KA RD AS SK VK 99+ PM

GAYATRI N... Karan Ahuja RUTUJA D... ANURADH... SHREYASH... VAISHNAVI... Participants

## Introduction of 3D Accelerator Cards

- ❑ Much of 3D processing is short algorithms repeated on a lot of data
  - pixels, polygons, textures, ...
- ❑ Dedicated accelerators with simple, massively parallel computation

Ordinary VGA Quake  
 Resolution: 320x200  
 Colors: 256  
 FFrame-rate: 30fps

OpenGL Quake on 3Dfx  
 Resolution: 640x480  
 Colors: 65,536  
 FFrame-rate: 30fps

A Diamond Monster 3D, using the Voodoo chipset (1997)  
 (Konstantin Lanzet, Wikipedia)

Participants

Type a name

Share invite

Waiting in lobby (1)

punit (Guest) Admitting... Meeting guest

In this meeting (111) Mute all

PRABHANJAN MORE Organizer

2155005

2155008

Aastha Kulkarni (Guest) Meeting guest

ABHISHEK KORE

ABHISHEK PATIL

Abhishek Sonwa... (Guest) Meeting guest

ADHIRAJ MALI

ADITYA GADHAVE

53:24

Take control Pop out Chat People Raise React View Rooms Apps More Camera Mic Share Leave

GN KA RD SK VK KP 99+ PM

GAYATRI N... Karan Ahuja RUTUJA D... SHREYASH... VAISHNAVI... KOMAL PA... Participants

GPU Overview No Label - Saved to this PC

## ChatGPT

- ❑ AI chatbot released by OpenAI
  - Based on large language models (LLMs) that can understand and generate natural language text based on a given input.
  - ChatGPT is designed to have human-like conversations and produce detailed and coherent responses
  - Most recent version trained on 1Trillion + parameters!
- ❑ According to a report by Tom's Hardware1, ChatGPT will eventually need over 30,000 Nvidia GPUs to process training data and deploy the model commercially!

Meeting chat

Saturday 10:38 am  
 REALConnect : RIT ETC Alumni Connect : Session on "Overview of GPU and its applications in Data Centers" : by Mr. Karan Ahuja (Alumni - 2016 Batch)

Today

10:13 am Meeting started

10:24 am Recording has started

11:02 am  
 You can ask questions in Chat box also

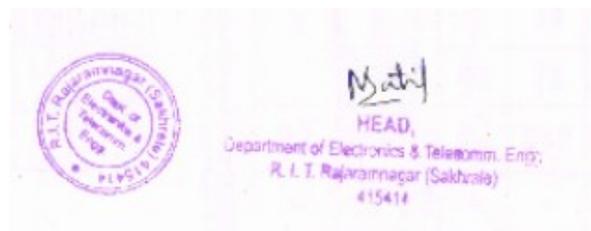
Feedback Link :

Type a new message



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The screenshot shows a Zoom meeting interface. At the top, there are controls for Chat, People, Raise, React, View, Rooms, Apps, More, Camera, Mic, Share, and a Leave button. The main area displays a grid of video thumbnails for participants: Karan Ahuja (top), Mahesh kumbhar (Guest) (bottom left), RAMESH PATIL (bottom middle), and a smaller thumbnail (bottom right). A 'Participants' list on the right shows avatars for GN, PD, VK, KP, SNEHA MU..., RUTUJA M..., SS, RR, SM, and 99+ others. A 'Meeting chat' window on the right contains a message from Saturday 10:38 am: 'REALConnect : RIT ETC Alumni Connect : Session on "Overview of GPU and its applications in Data Centers" : by Mr. Karan Ahuja (Alumni - 2016 Batch)'. Below this, it says 'Today' and lists '10:13 am Meeting started' and '10:24 am Recording has started'. A message at 11:02 am says 'You can ask questions in Chat box also' with a 'Feedback Link' below it. At the bottom of the chat is a 'Type a new message' input field.





Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
**(An Autonomous Institute)**

Alumni Industry expert lecture

On

**SOFTWARE TESTING**

*By*

**Mr. Prashant Khade**

**Under the aegis of**

**REALConnect**

**RIT ETC Alumni Connect**

A series of expert lectures for leveraging the power of alumni

*24<sup>th</sup> April 2023*

**Organised by**

**Electronics & Telecommunication Engineering**

Faculty Coordinator

**Dr. Mahesh Kumbhar**

Email: [Mahesh.kumbhar@ritindia.edu](mailto:Mahesh.kumbhar@ritindia.edu)

Contact: +91 9970700741



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## **About the Industry Expert Lecture**

**Software Testing** is a method to check whether the actual software product matches expected requirements and to ensure that software product is Defect free. It involves execution of software/system components using manual or automated tools to evaluate one or more properties of interest. The purpose of software testing is to identify errors, gaps or missing requirements in contrast to actual requirements. Some prefer saying Software testing definition as a White Box and Black Box Testing. In simple terms, Software Testing means the Verification of Application Under Test (AUT). This Software Testing course introduces testing software to the audience and justifies the importance of software testing.

## **About Resource Person**

Prashant Khade is alumni of 2016 Post graduate batch from RIT ETC Dept. He is currently working as a Assistant Professor in Adarsh Institute of Technology, Vita

## **Objective of the Expert Lecture**

To aware the students about Software testing and its need. In the session the following objectives are covered

- To find any defects or bugs that may have been created when the software was being developed
  - To increase confidence in the quality of the software
  - To prevent defects in the final product
  - To ensure the end product meets customer requirements as well as the company specifications
  - To provide customers with a quality product and increase their confidence in the company
- When it comes to testing software it's important to achieve the best possible results. To help with following the right strategy for testing there are certain principles that need to be followed.

## **Expected Outcomes of the Expert Lecture**

After completion of the Session, students will be able to remember benefits of software testing:

**Cost-Effective:** It is one of the important advantages of software testing. Testing any IT project on time helps you to save your money for the long term. In case if the bugs caught in the earlier stage of software testing, it costs less to fix.



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**Security:** It is the most vulnerable and sensitive benefit of software testing. People are looking for trusted products. It helps in removing risks and problems earlier.

**Product quality:** It is an essential requirement of any software product. Testing ensures a quality product is delivered to customers.

**Customer Satisfaction:** The main aim of any product is to give satisfaction to their customers. UI/UX Testing ensures the best user experience.

Total Participants: TY Electronics and Telecommunication Engg. - 55

### Poster of the Expert lecture

**REAL Connect**  
**RIT ETC Alumni Connect**  
*Leveraging the power of Alumni*

**Department of Electronics and Telecommunication Engineering**  
**Rajarambapu Institute of Technology,**  
**Rajaramnagar**

**Topic: Software Testing**

**Mr. Prashant Khade**  
2017 Batch, M.Tech Digital System

**Monday,**  
24th April, 2023

**1 P.M-2 P.M**

**CR 25, ETC Dept., RIT**



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(An Autonomous Institute)

### Glimpses of the Workshop



  
*Mutli*  
HEAD,  
Department of Electronics & Telecomm. Engg,  
R. I. T. Rajaramnagar (Sakhsalg)  
415414



Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
**(An Autonomous Institute)**

Alumni Industry expert lecture

On

**Cloud Computing**

**Under the aegis of**

**REALConnect**

**RIT ETC Alumni Connect**

A series of expert lectures for leveraging the power of alumni

*22<sup>nd</sup> June 2023*

**Organised by**

**Electronics & Telecommunication Engineering**

Faculty Coordinator

**Mrs.V.S.Patil**

Email: [Vilabha.mane@ritindia.edu](mailto:Vilabha.mane@ritindia.edu)

Contact: +91 9970700924



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**Rajarambapu Institute of Technology, Rajaramnagar**  
**(An Autonomous Institute)**

### **About the Industry Expert Lecture :**

At the beginning of 2020 , as the COVID -19 crisis unfold , businesses began to accelerate their digital transformation to cloud to help navigate the human and business impact. It is clear that working from home and business continuity have been made possible by cloud computing. The importance of cloud technology is even more apparent when we look at the performance gap that already exists between enterprise technology leaders and laggards. Almost overnight the gap has been widened.

### **About Resource Person:**

Sachin is graduated in 2022 in E&TC stream and currently working as a Programmer Analyst at Cognizant. He has completed six months Internship at Cognizant in 'Full Stack Data Integration' domain from Jan-June' 2022. He has trained and certified by Microsoft in below four Azure Cloud tracks:

- Azure Fundamentals
- Azure DATA fundamentals
- Azure AI fundamentals
- Azure Data Engineer

### **Objective of the Expert Lecture:**

- To aware the students about cloud computing types, cloud computing services.
- To inspire the students to start the cloud computing.

### **Contents:**

- Cloud Computing basics
- Cloud providers
- Data Center
- How to start the cloud journey

### **Expected Outcomes of the Expert Lecture :**

After completion of the workshop, students will be able to

- Explain fundamentals of Cloud Computing.
- Differentiate cloud types.
- Describe the cloud services



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**Rajarambapu Institute of Technology, Rajaramnagar**

**(An Autonomous Institute)**

**No. of Participants:**

- T.Y. B Tech ETC: 58

**Poster of the Expert lecture:**

**REALConnect**  
**RIT ETC Alumni**  
**Leveraging power of Alumni**

**Department of Electronics and Telecommunication Engineering**  
**Rajarambapu Institute of Technology,**  
**Rajaramnagar**

**Mr. Sachin Jalwadi**  
**Programmer Analyst**  
**Cognizant, Pune**

**Topic: Cloud Computing**  
 **Thursday, 22<sup>nd</sup> June 2023**  
 **2pm to 3pm**  
 **CR .25**

**Attendance of Expert Lecture:**



Kasegaon Education Society's

Rajarambapu Institute of Technology, Rajaramnagar

(An Autonomous Institute)

Expert Lecture by Alumni

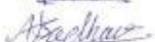
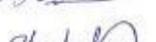
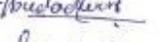
Date: 22nd June 2023

Topic: Cloud Computing

Sr.No	Name of student	Sign
1	Vishal Munasing shevade	Vishal
2	Adhishaj Sachidanand Mali	Adhishaj
3	Chaitanya R. Bijaragi	Chaitanya
4	Satyajit Panajit Skindar	Satyajit
5	Apuv D. Mahajan	Apuv
6	Prasad P. Baharr	Prasad
7	Suraj B Kithure	Suraj
8	Shubham P. Patil	Shubham
9	Harshvardhan M. Patil	Harshvardhan
10	Abhishhek S. Kore	Abhishhek
11	Anant A. chougule	Anant
12	Rohit t. Nark	Rohit
13	Tanmay. s. Kamble	Tanmay
14	Punit D. Pawar	Punit
15	Prathamesh Patil	Prathamesh
16	nabesh Bandgar	Nabesh
17	Arnav Khanaj	Arnav
18	Shubham Patil.	Shubham
19	Om Khulape	Om
20	Paree Vambare	Paree
21	Sainath Khanar	Sainath
22	Jeevan Patil	Jeevan
22	Prateek Kamble	Prateek
24	Harshal Thorat	Harshal
25	Suhais kumbhoikar	Suhais
26	Tejas Bhaskar Jagtap	Tejas
27	Prathamesh Prakash mane	Prathamesh
28	Aditya Namdev Gadhave	Aditya
29	sumeru S. Chougule	Sumeru
30	Vikas Rohidas Pawar	Vikas
31	Chougule Archana Anil	Archana
32	Pachangane Tejal	Tejal
33	Bhagyashree S. Patil	Bhagyashree
34	sanjana S. Yadav	Sanjana
35	Darshana Mohan Patil.	Darshana
36	Anjali Rajendra Jadhav	Anjali
37	Anushka Rahul Ligade	Anushka
38	Dirha Ghoshikant Gadekar	Dirha
39	Devpriya Sunil had	Devpriya
40	Pranali Dinkar Magdum	Pranali
41	Komal Prakash Jadhav	Komal
42	Nikita shiwaji Jadhav	Nikita
43	shanti Yuvraj kashji	Shanti
44	Afrin Akbar Shaikh	Afrin
45	Vaishnavi Vikas Patil	Vaishnavi
46	Sai G. kulkaeni	Sai
47	Sanika S. Patil	Sanika
48	Janhavi M. Kadam	Janhavi
49	Manasvi m. Gondhale	Manasvi
50	Indrayani T. patil	Indrayani
51	Arantika S. Thavale	Arantika



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52	Nikita S. Saha	
53	Shravani P. Chavan	
54	Mulani Nazneen	
55	Tejaswri Bhale	
56	Ankita Jadhav	
57	Bhakti.B. Badadwar	
58	Lakshmi. A. Kharavane	

**Glimpses of the expert lecture:**





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*M. S. Patil*  
HEAD,  
Department of Electronics & Telecomm. Engg,  
R. L. T. Rajaramnagar (Sakhrisig)  
415414

K.E.Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
**Electronics and Telecommunication Engineering**



## Report of Social Worker Yogeshwari Patil

### 1. Advertisement of the event (flyer):



### 2. Expert short information:

**NGO, in Quality Education Support Trust (QUEST) Palghar, Raigad .** QUEST has designed various programs for tribal and rural schools, teachers and teacher educators. The idea of QUEST took shape in an informal group of education enthusiasts called the 'Thane Study Circle' and now runs different education programs in government schools across 24 districts in Maharashtra.

**Working in Raigad District.**

**Linkdin Profile Link:**

**<https://www.linkedin.com/in/yogeshwari-d-patil-0029b9142>**

Rajarambapu Institute of Technology, Rajaramnagar, SAKHARALE Bachelor of Technology, Electronics and Communications Engineering · (2015 - 2019)

### **3. Course details**

**Title of program: “Role of Engineers in Social work”**

**Objectives:**

1. Enabling engineers to identify social issues, understand their underlying causes, and develop effective solutions.
2. Equipping them with the skills with diverse teams, and drive impactful outcomes.
3. Implementing smart solutions for efficient resource management.

**Benefit in terms of learning/Skill/Knowledge obtained:**

Engaging in social work provides students with an opportunity to apply their theoretical engineering knowledge in real-world contexts, reinforcing their understanding and enhancing their problem-solving skills. Social work exposes students to diverse social and cultural realities, fostering empathy and a broader perspective on societal challenges. This holistic approach can help shape engineers who are not only technically proficient but also socially conscious. Engaging in social work can contribute to personal growth by fostering leadership qualities, communication skills, and adaptability. Students can develop a sense of social responsibility and gain a deeper understanding of the impact their work can have on society.

- **No. of students attended: 62**
- **No. of faculties attended: 03**

### **4. Short report:**

The report outlines the role of engineers in social work, highlighting how engineering knowledge can support social objectives. It also addresses the benefits of engaging in social work for second-year engineering students, focusing on the valuable learning, skills, and knowledge they can acquire. Engineers play a crucial role in addressing societal challenges and promoting positive change. Engineers have a vital role to play in social work, utilizing their technical expertise and problem-solving skills to contribute to societal betterment. For second-year engineering students, engaging in social work offers invaluable opportunities for learning, skill development, and acquiring knowledge that extends beyond the classroom. By integrating engineering knowledge with social objectives, engineers can make a lasting impact on communities and help build a more sustainable and equitable world.

## 5. Photographs:





**K.E. Society's**

**Rajarambapu Institute of Technology, Sakharale**

*(An Autonomous Institute, affiliated to Shivaji University, Kolhapur)*

**DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGINEERING**

One Day Workshop

On

**Structured Query Language (SQL)**

*Saturday, 3<sup>rd</sup> December 2022*

Coordinators

**Prof. Arati A. Pujari**

Email: arati.pujari@ritindia.edu

Contact: +91 7387920772

**Prof. Prabhanjan P. More**

Email: prabhanjan.more@ritindia.edu

Contact: +91 7276868180

### **Objectives of the Workshop**

1. To aware students about the different concepts of SQL.
2. To perform different operations on database to store, manipulate and retrieve data from database.

### **About the Workshop**

In the morning session of workshop, process of installation of SQL server 2019 and SQL server management studio covered. Also basics of SQL, different SQL statement and how to perform basic operations on table using SQL statement has covered. The concept of storing, manipulating and retrieving stored data in a relational database has covered.

In the afternoon session, different points such as clauses, keywords, operators and constraints in SQL are covered. Difference between join and union commands are also covered.

These points helped student to understand database management concepts and will help them in their placement activity and drives of IT companies like Wipro, Zensar, Capgemini etc.

## **Workshop Contents**

- What is SQL?
- What SQL can do?
- Database and table
- SQL statements
- Clauses
- Wildcard
- Aggregate functions
- Keywords
- Constraints
- Union and Union all
- Join

## **Workshop Outcomes**

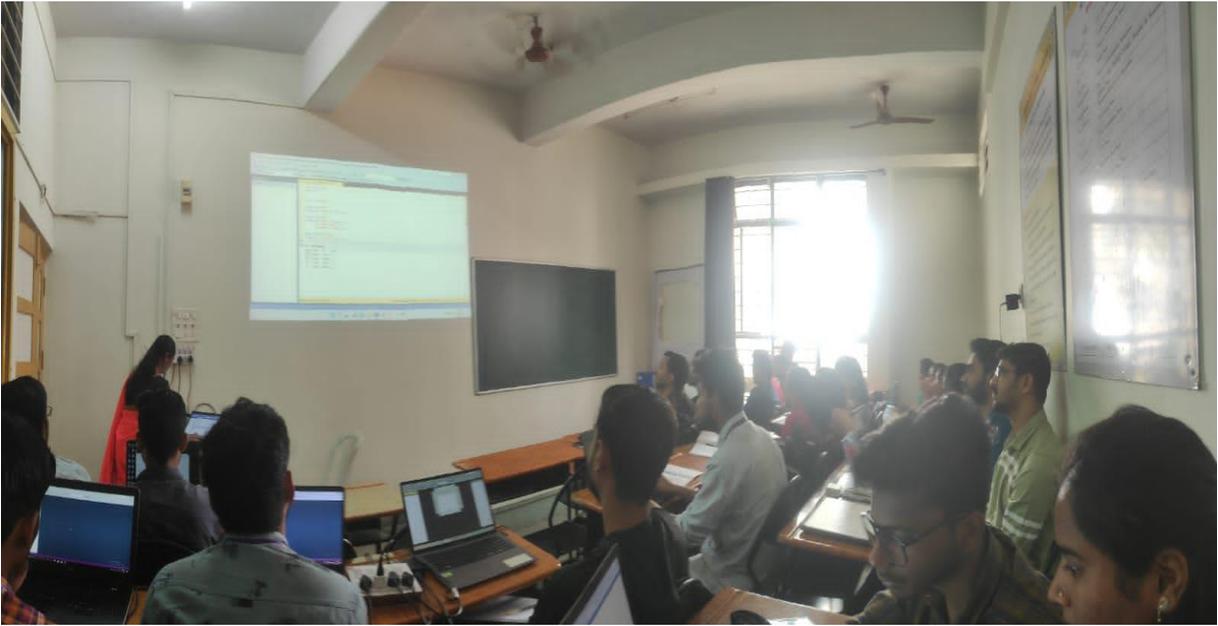
**After completion of workshop, students are able to**

1. Define SQL.
2. Differentiate between DBMS and RDBMS.
3. Create tables and database.
4. Execute different queries against table.
5. Use different clauses to obtain specific records from table.
6. Sort data according to the required conditions.
7. Apply different constraints on data to be stored in table
8. Combine two or more tables.

**No. of participants : 19**

**Fee Structure : Rs. 100/- per student**

**Photos:**



  
*Mutli*  
HEAD,  
Department of Electronics & Telecomm. Engg.,  
R. I. T. Rajaramnagar (Sakhring)  
415414



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One Day Workshop  
On  
**Circuit Simulation Using Proteus Simulation Tool**  
**Let's Simulate**

*18<sup>th</sup> March 2023*

Organised by  
**Electronics & Telecommunication Engineering Students**  
**Association (ETESA)**

under  
**Lead College Scheme,**  
**Shivaji University Kolhapur**



Faculty Coordinator

**Prof. S.R.Watharkar**

Email: [snehal.watharkar@ritindia.edu](mailto:snehal.watharkar@ritindia.edu)

Contact: +91 7709889174





## Session II: Circuit Simulation using Proteus.

- Introduction to Proteus schematic tool ISIS
- Analog Electronics Circuit simulation using Proteus
- Digital Electronics Circuit simulation using Proteus.

## Workshop Registration link

[https://docs.google.com/forms/d/e/1FAIpQLSd7oR9wVH2e6XRieg8OsZOuBhuAXRVfqvOVIrSdvL\\_990ywg/viewform?usp=sf\\_link](https://docs.google.com/forms/d/e/1FAIpQLSd7oR9wVH2e6XRieg8OsZOuBhuAXRVfqvOVIrSdvL_990ywg/viewform?usp=sf_link)

## Expected Outcomes of the Workshop

After completion of the workshop, students will be able to

- Test the basic electronics components using multimeter.
- Analyze analog electronic circuit using mathematical approach.
- Simulate the circuit in simulation tool.

## No. of Participants

- S.Y. ETC: 43
- S.Y. Electrical: 62
- T.Y. Mechatronics: 01
- Total Participants: 106

## Feedback

<https://docs.google.com/spreadsheets/d/13D3msww->

[5UeQzA2qGgKPH\\_sMM6nfddiHJxfNOhNUW4A/edit?resourcekey#gid=103975455](https://docs.google.com/spreadsheets/d/13D3msww-5UeQzA2qGgKPH_sMM6nfddiHJxfNOhNUW4A/edit?resourcekey#gid=103975455)

2105067	Mayur Anil Jadhav	S.Y.ETC	Excellant	We are greatfull by attending this workshop. We have learned the most helpful things by attending this workshop
2108030	Abhijeet Dnyanba Dhapa	S.Y.ELECTRICAL	Excellant	We study lots of things from workshop
2106075	Aniket Bharat Sid	S.Y.ETC	Excellant	.
2108017	Pratik Ravindra Thombar	S.Y.ELECTRICAL	Excellant	I like this session very much 😊
2108019	Shakil sikandar wankar	S.Y.ELECTRICAL	Excellant	I like these session very much



2105022	Aastha Prashant Kulkarni	S.Y.ETC	Very Good	Today's workshop is very helpful to acquire basic knowledge about proteus softwear. I am very satisfied
2105017	Sanika Ramesh Patil	S.Y.ETC	Very Good	Good teaching and helpful
2108010	Sanket Rajesh Patil	S.Y.ELECTRICAL	Excellant	It is very helpfull workshop
2105050	Vidya Agatrao Khandeka	S.Y.ETC	Excellant	Workshop was amazing, experience is to good, it is very useful for projects.
2108037	Sahil samadhan jadhav	S.Y.ELECTRICAL	Excellant	Better
2105007	Shreya Santosh Khochag	S.Y.ETC	Excellant	Workshop was very good.I get much knowledge from workshop.
2108016	Chandrasen Mahadev sa	S.Y.ELECTRICAL	Excellant	Workshop is very good.....
2108027	Vivek Mahesh Jangam	S.Y.ELECTRICAL	Very Good	Good
2105046	Prajwal prakash patil	S.Y.ETC	Very Good	Good
2108020	Shruti Sujit Pawar	S.Y.ELECTRICAL	Excellant	More such workshop should be arranged
2108047	NEHA NARAYAN SATYA	S.Y.ELECTRICAL	Very Good	This workshop benefits to us.. 🙌
2108068	Atharv Mahendra Phalle	S.Y.ELECTRICAL	Excellant	Workshop is good.I want to know about protes. management was very good. Seniors helps us to know about the app.
2108003	Aditya Shankar Gherade	S.Y.ELECTRICAL	Excellant	Very informative
2108035	Prajakta Ramchandra Ja	S.Y.ELECTRICAL	Very Good	The software is used mainly by electronic design engineers and technicians to create schematics and electronic prints for manufacturing printed circuit boards.
2105034	Tejas Maruti Shinde	S.Y.ETC	Excellant	The section was best we learn new concept but the teaching style is like that so we can not recognise that we learning the simulation first time



Kasegaon Education Society's

Rajarambapu Institute of Technology, Rajaramnagar

(An Autonomous Institute)



## Poster Of the Workshop

K. E. Society's  
**RAJARAMBAPU INSTITUTE OF TECHNOLOGY**  
 Rajaramnagar, Sakharale, Tal-Walwa Dist- Sangli - 415414

**Electronics And Telecommunication Engineering Student's Association**  
 Presents  
 One Day Workshop

*Let's  
 Simulate*  
 'Stepping into the world of simulation'

Under the Lead College Scheme Shivaji University Kolhapur

**18th March 2023, Saturday.**  
**10 A.M to 5 P.M**  
**CSIT Conference Hall**

For any queries contact - 9970687652

Scan here to register

- Certificates for all participants
- Hands on experience
- Tea for all participants

Mr. Mahesh Bandgar  
President, ETESA

Mrs.S.R.Watharkar  
Faculty coordinator ETESA

Mr. M.S.Patil  
HOD, E&Tc

Dr.D.G.Thombare  
Coordinator Lead College Scheme ,SUK

Dr. Mrs.S.S.Kulkarni  
Director , RIT

## Certificate of Participation

**CERTIFICATE  
 OF PARTICIPATION**

THIS CERTIFICATE IS AWARDED TO

Has successfully completed one day workshop 'Let's Simulate' on circuit simulation using Proteus software, Organised by Electronics and Telecommunication Engineering Student's Association (ETESA) under Lead College Scheme, Shivaji University Kolhapur held on 18th March 2023.

**Congratulations !**

**Mr. Mahesh Bandgar**  
 President  
 ETESA

**Prof.S.R.Watharkar**  
 Faculty Coordinator  
 ETESA

**Prof.Dr.M.S.Patil**  
 HOD, ETC

**Dr. D. C. Thombare**  
 Coordinator,  
 Lead College Scheme, SUK

**Dr.Mrs.S.S.Kulkarni**  
 Director ,RIT



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**Rajarambapu Institute of Technology, Rajaramnagar**  
(An Autonomous Institute)



## Glimpses of the Workshop



  
*Mutli*  
HEAD,  
Department of Electronics & Telecomm. Engg.  
R. I. T. Rajaramnagar (Sakhrnag)  
415414



Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
**(An Autonomous Institute)**

Alumni Expert Lecture  
On  
**Intellectual Property Awareness**

Under the aegis of  
**REALConnect and RIT IIC cell**  
**RIT ETC Alumni Connect**

A series of expert lectures for leveraging the power of alumni

*30<sup>th</sup> April 2023*

Organised by  
**Electronics & Telecommunication Engineering**

Faculty Coordinator

**Dr. Jayashree S. Awati**

Email: [jayashree.awati@ritindia.edu](mailto:jayashree.awati@ritindia.edu)

Contact: +91 8600009767



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### About the IPR Expert Lecture:

On the occasion of World Intellectual Property Rights (IPR) Day, an expert lecture on Intellectual Property Awareness was delivered to students. The lecture focused on the importance of intellectual property, including patents, trademarks, and copyrights, and the need for individuals and businesses to protect their intellectual property rights. The speaker emphasized the role of intellectual property in driving innovation and creativity and highlighted the various legal tools available to protect it. The lecture was a great opportunity for students to gain a better understanding of intellectual property and how it impacts their lives and careers. Overall, it was an informative and enlightening event that helped raise awareness about the importance of intellectual property.

### About Resource Person

#### **Mrs. Saylee Sandeep Bidwai**

Head-Entrepreneurship Development Cell, MITAoE,

[Alandisaylee.bidwai@mitaoe.ac.in](mailto:Alandisaylee.bidwai@mitaoe.ac.in), [sayleebidwai@gmail.com](mailto:sayleebidwai@gmail.com)

9011587612(WhatsApp) / 91-9423351251

<https://www.linkedin.com/in/saylee-bidwai-12aa52b6/>



#### **Awards and achievements:**

- **Awarded as Distinguished Assistant Professor by Computer Society of India** (15<sup>th</sup> January 2017)
- **Indian Achievers' Award for Excellence in Education, 2021-22**, awarded by Indian Achievers' Forum & Achievers' World for contribution in nation building, (31<sup>st</sup> January 2022)
- **MoU and Center of Excellence of eInfochips Training and Research Organization**. Generated revenue of 9.4 Lakh (July 2015-June 2018)
- Intellectual Property Associate and subject matter expert at AM Legal Associates, Pune
- **Silver Elite Certification** from NPTEL for the 4-week course- **Managing Intellectual Property in Universities** (July-Aug 2021)
- Executive director of Samruddhi **Technology Business Incubator** through which, encouraged and holding hands of **8 start-ups**
- **Industrial Design 'Composter machine'** (Design no.-345316-001)



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**Qualification details:**

- Pursuing **PhD** from Visvesvaraya Technological University, Belgaum. Research area: Hybrid architecture for **Network on Chip**
- **ME Electronics** from Shivaji University, Kolhapur (2010): **65.33%** (First Class)
- **BE Electronics** from SRTM University, Nanded (2005): **69.06%** (First Class with Distinction)
- Diploma in **Intellectual Property Law** with **CGP 7.75** (2018-19)
- Certified by NIIPM, Nagpur on **Patent description, claims, drafting, search, trademark, GI filing, copyright and designs filing in India**, December 2019
- **NPTEL Certification** for the 12-week course – **Patent Law for Engineers and Scientists** (Jan-Apr 2019)
- Certificate in **IP commercialization and licensing** from **RGNIIPM** in association with **Maharashtra National law University Mumbai**.

**Experience:**

- **17 Years** Academic experience (Electronics Engg) + **4 years** IPR
- Specialization in **Patent Drafting** and litigation. Worked with MNCs, foreign clients and local industries as well.
- Experience in Patent research, scrutinizing inventions for patentability, Patent search, writing FER responses, well acquainted with office processes of Indian Patent Office
- **IPR expert** in Institute Innovation and Startup Policy (IISP) of KIT's College of Engineering, Kolhapur
- Coordinator-**National Innovation and Startup Policy** at MITAoE, Pune
- IPR awareness programs conducted: 2 for industry associations, 10 for academics

**Objectives of the Expert Lecture:**

- To provide a comprehensive understanding of the different types of IPR, such as patents, copyrights, trademarks, and trade secrets.
- To explain the importance of IPR in protecting the creative and innovative works of individuals and businesses and its role in encouraging economic growth and supporting a thriving market for goods and services.
- To discuss the challenges involved in dealing with IPR, such as infringement and enforcing IPR laws across borders, and to provide insights on how these challenges can be addressed.



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**(An Autonomous Institute)**

**Contents:**

- IPR Introduction
- Patent
- Copyright
- Trademarks
- Success stories, etc.

**Expected Outcomes of the Expert Lecture:**

After completion of the workshop, students will be able to

1. Intellectual Property Rights (IPR) refer to legal rights that protect individuals or businesses' creative and innovative works.
2. Various types of IPR include patents, copyrights, trademarks, and trade secrets, which provide exclusive rights to the owners for a specific period.
3. Understanding IPR is crucial for encouraging economic growth and supporting a thriving market for goods and services.
4. Challenges in dealing with IPR include infringement, determining boundaries of protection, and enforcing IPR laws across borders.

**No. of Participants:**

Total Participants: 56



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Poster of the Expert lecture

# REAL Connect

RIT ETC Alumni Connect  
Leveraging the power of Alumni



Department of Electronics and Telecommunication  
Engineering  
Rajarambapu Institute of Technology,  
Rajaramnagar



Joining Link. <https://bit.ly/3LEIb8Z>



Topic: Intellectual Property  
Rights Awareness



Sunday,  
30th April, 2023



11 A.M-12 P.M



Online

Prof. Sayali Bidwai  
2009 Batch, M.Tech  
Electronics



Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
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## Glimpses of the Workshop

The screenshot shows a Microsoft Teams meeting interface. The main content area displays a slide titled "Concept of IP" with the following bullet points:

- Intellectual Property = creations of the mind
- Similar to a one's personal property
- Based on laws- Patent's act, copyright law, design act, trademarks act and rules for each
- Inventions; literary and artistic works; symbols, names and images used in commercial world

The meeting controls at the top show the time as 55:10. The participants list on the right includes: JAYASHREE AWATI (Organizer), 2155001, 2155007, 2428 Roshana W... (Guest), and ABHISHEK KORE. The taskbar at the bottom shows the system tray with a temperature of 26°C and the date 30-04-2023.

The screenshot shows a Microsoft Teams meeting interface. The main content area displays a slide titled "IP rights" with the following bullet points:

- Allow creators, or owners of patents, trademarks or copyrighted works to **benefit** from their own work or investment in a creation
- The **legal protection** of new creations encourages the commitment of additional resources for further innovation
- Promotion and **protection** of intellectual property spurs **economic growth**, creates new jobs and industries, and enhances the quality and enjoyment of life

The meeting controls at the top show the time as 58:03. The participants list on the right includes: JAYASHREE AWATI (Organizer), 2155001, 2155005, 2155007, and 2428 Roshana W... (Guest). The taskbar at the bottom shows the system tray with a temperature of 26°C and the date 30-04-2023.



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The screenshot shows a Microsoft Teams meeting interface. The main content is a slide titled "Classification of IPs" with the following text:

- Industrial Property**
  - Patents for inventions, Trademarks, Industrial designs and Geographical Indications.
- Copyright**
  - Novels, poems and plays; films, music, artistic works (e.g., drawings, paintings, photographs and sculptures) and architectural design
  - Rights related to copyright include those of performing artists in their performances, producers of phonograms in their recordings, and broadcasters in their radio and television programs
- Trade secrets**

The meeting controls at the top show a timer at 01:03:40 and various icons for chat, people, raise hand, react, view, apps, more, camera, mic, share, and leave. The participants list on the right includes Saylee Bidwai (SB), VAISHNAVL... (VK), and 44 other participants. The bottom of the screen shows a Windows taskbar with the date 30-04-2023 and time 11:18.

The screenshot shows a Microsoft Teams meeting interface. The main content is a slide titled "IPs at a glance" with a table summarizing different types of intellectual property:

Legal right	For What?	How?	Period
Patents	Inventions	Application and examination	20 Yrs
Trade marks	Distinctive identification of products or services	Use and/or registration	10Yrs
Industrial designs	Aesthetics/ ornamental shape/appearance	Registration	15 years
Copyright	Literary and artistic woks	Exists automatically, register	Lifetime + 60 Yrs
Trade secrets	Valuable information not known to the public	Reasonable efforts to keep secret	No limit

The meeting controls at the top show a timer at 01:06:31. The participants list on the right includes Saylee Bidwai (SB), VAISHNAVL... (VK), and 45 other participants. The bottom of the screen shows a Windows taskbar with the date 30-04-2023 and time 11:21.



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The screenshot shows a Microsoft Teams meeting interface. The main content is a slide titled "IPs at a glance" which is a flowchart. It maps "Legal right" to "For What?" and "How?".

Legal right	For What?	How?	Period
Patents	Inventions	Application and examination	20 Yrs
Trade marks	Distinctive identification of products or services	Use and/or registration	10Yrs
Industrial designs	Aesthetics/ ornamental shape/appearance	Registration	15 years
Copyright	Literary and artistic works	Exists automatically, register	Lifetime + 60 Yrs
Trade secrets	Valuable information not known to the public	Reasonable efforts to keep secret	No limit

The meeting interface includes a top navigation bar with "Chat", "People", "Raise", "React", "View", "Apps", and "More". A "Participants" panel on the right shows 49 participants, including the organizer JAYASHREE AWATI and several guests. The bottom taskbar shows the system tray with a temperature of 26°C and the date 30-04-2023.

The screenshot shows a Microsoft Teams meeting interface. The main content is a slide titled "Patentability" with a bulleted list of criteria.

- Only for technological invention
- Novelty, inventive step, industrial applicability
- Inventive improvement in existing patent
- What **can't** be patented ?
  - Inventions contrary to natural laws, public order/morality, inventions relating to atomic energy
  - Scientific theory/ principle
  - Mere discovery of living/nonliving thing that is naturally existing
  - New form/admixture/property/use of known substance

The meeting interface includes a top navigation bar with "Chat", "People", "Raise", "React", "View", "Apps", and "More". A "Participants" panel on the right shows 55 participants, including the organizer JAYASHREE AWATI and several guests. The bottom taskbar shows the system tray with a temperature of 26°C and the date 30-04-2023.



Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
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Microsoft Teams meeting interface showing a slide titled "Geographical indications". The slide content includes:

- A sign used on goods that have a specific **geographical origin** and **possess qualities** or a reputation due to that **place of origin**

Images shown on the slide:

- DARJEELING TEA
- Kashmir shawl
- Nagpur orange

Participants list:

- JAYASHREE AWATI (Organizer)
- 2005047 (Guest)
- 2155001
- 2155005
- 2155007

System tray: 26°C Cloudy, 11:46, 30-04-2023

Microsoft Teams meeting interface showing a slide titled "Success Stories". The slide content includes:

- GreenSole**  
Step towards sustainability
- Youngest Patent-holder on wheelchair**
- JAIPUR: Drawing inspiration from scientist Stephen Hawking's a wheelchair-bound nine-year old boy here has invented a game of six-player circular chess. The boy, Heidageshwar Singh Bhati has got the game's design patented in his name.

Participants list:

- JAYASHREE AWATI (Organizer)
- 2005047 (Guest)
- 2155001
- 2155005
- 2155007

System tray: 26°C Cloudy, 11:48, 30-04-2023



Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
**(An Autonomous Institute)**

Microsoft Teams interface showing a meeting with 56 participants. The main content area displays a slide titled "Key take-away" with the following bullet points:

- For researchers and faculty- patents, copyrights
- For students (engineering, pharmacy, science / technology background)
- For industries- startups
- For institutes- NIRF ranking, ARIYA ranking, NBA, NAAC
- For society

The meeting controls include Chat, People, Raise, React, View, Apps, More, Camera, Mic, Share, and Leave. The participant list includes JAYASHREE AWATI (Organizer), 2005047 (Guest), 2155001, 2155005, 2155007, 2428 Roshana W... (Guest), and ADHIRAJ MALI.

**MSteam Link:**

[https://ritindiaedu.sharepoint.com/sites/WebinarStepbyStep/Shared%20Documents/General/Recordings/Expert%20Lecture%20by%20Prof.%20Sandeep%20Bidwai-20230113\\_151643-Meeting%20Recording.mp4?web=1](https://ritindiaedu.sharepoint.com/sites/WebinarStepbyStep/Shared%20Documents/General/Recordings/Expert%20Lecture%20by%20Prof.%20Sandeep%20Bidwai-20230113_151643-Meeting%20Recording.mp4?web=1)





Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
(An Autonomous Institute)



Line Follower Robot competition  
**ROBOCAD MICROMOUSE**

*17<sup>th</sup> March 2023*

Organised by  
**Electronics & Telecommunication Engineering Students  
Association (ETESA)**

under  
**Lead College Scheme,  
Shivaji University Kolhapur**



Faculty Coordinator

**Prof. S. R. Watharkar**

Email: [snehal.watharkar@ritindia.edu](mailto:snehal.watharkar@ritindia.edu)

Contact: +91 7709889174



## About the Event

'ROBOCAD MICROMOUSE' is one of the grand and the most accomplished event of Electronics and Telecommunication Department. It is a line follower robot competition. Students from different polytechnic and Engineering colleges participate in this competition. As of this year, the competition was held on 17<sup>th</sup> March, 2023. Apart from RIT, a huge participation was seen among students from the colleges like DKTE(Ichalkaranji), SIT(Yadrav), Government college of Engineering (Karad) and Walchand college of Engineering (Sangli).

The Inaugural function was sharp at 10:30 a.m. at CSE porch. Director Dr. Mrs. S. S. Kulkarni was invited as chief guest for the inaugural function. Inauguration was done by cutting the Ribbon. The felicitation of Director Mam was done by Dr. M. S. Patil, HoD ETC. The Deans, HoDs and Teaching faculty was also felicitated by the ETESA Members. After Inaugural function, Director mam motivated the students and participants to actively participate in such competitions to get benefited. She briefed about the importance of such competitions.

The competition was started at 11:00 A.M. 30 groups participated in competition i.e., 60+ students participated. The competition was held in 2 rounds. Both the rounds had the same rules but the track that Robot followed, distinguished the rounds. The students would be given a pre-defined maze to follow. The robot should complete the maze within minimum time and minimum penalties. There were number of junctions. From the First round 20 groups were eliminated and the remaining 10 groups were shortlisted for the second round. From those the first three who got the maze completed were chosen Winners of the competition.

Prize distribution ceremony was held at Electronics and Telecommunication Department. All the faculty members were invited. The winners were declared and they awarded with a prize money and certificates. After the felicitation of winners was done, Mr. Mahesh Bandgar, President ETESA expressed the vote of thanks and the event was concluded.

## Objective of the Event

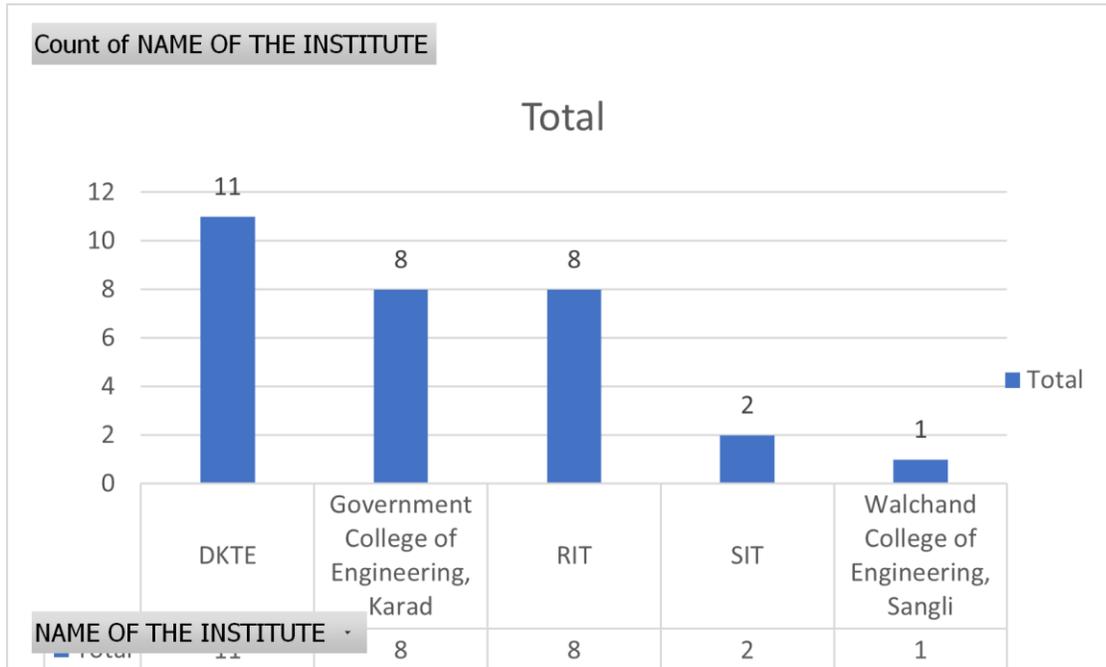
- To create a platform for students to showcase their technical skills
- To encourage competitive spirit among the students.

## Event Registration link

[https://docs.google.com/forms/d/e/1FAIpQLSemr6ESAbRdVctZXIY9ak0oRuZgj6Omi4X MhLM8PjDkDrC2uA/viewform?usp=sf link](https://docs.google.com/forms/d/e/1FAIpQLSemr6ESAbRdVctZXIY9ak0oRuZgj6Omi4X MhLM8PjDkDrC2uA/viewform?usp=sf_link)



## No. of Institutes Participated



## Winner List

Rank	Team No.	Team Members	Name of the Institute	Cash Prize
1	R4	Abhishek Sakhare Varun Gurav	DKTE	Rs.2500/-
2	R3	Aditya Pawar Vinay Shinde	RIT	Rs.2000/-
3	R7	Prabhat Hiremath Aniket Kengar	DKTE	Rs.700/-

## Feedback

[https://docs.google.com/forms/d/e/1FAIpQLSdSXFLC05J5K\\_1jnXOgZZsWPwWmybV5hR7J8lw\\_bn6bOuM49g/viewform?usp=sf\\_link](https://docs.google.com/forms/d/e/1FAIpQLSdSXFLC05J5K_1jnXOgZZsWPwWmybV5hR7J8lw_bn6bOuM49g/viewform?usp=sf_link)



Kasegaon Education Society's

Rajarambapu Institute of Technology, Rajaramnagar



(An Autonomous Institute)

### Poster Of the Event

K.E.Society's  
**RAJARAMBAPU INSTITUTE OF TECHNOLOGY**  
 Rajaramnagar, Sakharale Tal. Walwa Dist. Sangli – 415 414.

Department of Electronics and Telecommunication Engineering  
 Presents

**ROBOCAD**  
**LINE FOLLOWER ROBOT COMPETITION**  
**MICROMOUSE**

Rules and Regulations

17<sup>th</sup> March 2023  
 10:00 am

CSE Porch,  
 RIT Rajaramnagar

9139227815  
 Paras Vanarse

Under The Lead College Scheme, Shivaji University Kolhapur

**FREE ENTRY**  
**Win Exciting Cash Prizes !!**

Scan here to register

Mr. Mahesh Bandgar President ETESA	Prof. S.R. Watharkar Faculty co-ordinator ETESA	Dr.M.S.Patil HOD E&TC	Dr.D.G.Thombare Co-ordinator Lead College Activity, SUK	Dr.Mrs.S.S.Kulkarni Director RIT
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### Certificate of Participation

**CERTIFICATE**  
**OF PARTICIPATION**

THIS CERTIFICATE IS AWARDED TO

Has successfully completed one day workshop 'Let's Simulate' on circuit simulation using Proteus software, Organised by Electronics and Telecommunication Engineering Student's Association (ETESA) under Lead College Scheme, Shivaji University Kolhapur held on 18th March 2023.

Congratulations !

 Mr. Mahesh Bandgar President ETESA	 Prof.S.R.Watharkar Faculty Coordinator ETESA	 Prof.Dr.M.S.Patil HOD. ETC	 Dr. D. G. Thombare Coordinator, Lead College Scheme, SUK	 Dr.Mrs.S.S.Kulkarni Director .RIT
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## Glimpses of the Event





Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
(An Autonomous Institute)





Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
(An Autonomous Institute)



17.03.2023 12:27



17.03.2023 15:36



Kasegaon Education Society's  
**Rajarambapu Institute of Technology, Rajaramnagar**  
(An Autonomous Institute)



  
*Matri*  
HEAD,  
Department of Electronics & Telecomm. Engg.  
R. I. T. Rajaramnagar (Sakhrala)  
415414

**K. E. Society's  
Rajarambapu Institute of Technology (RIT), Rajaramnagar (Islampur) (Estd.1983)  
(An Autonomous Institute, affiliated to Shivaji University, Kolhapur)**

**Two-week Internship Program**

**VLSI FRONT END DESIGN**

**From: 8<sup>th</sup> June 2023 Onwards**

**Eligibility Criteria:**

All undergraduate/graduate students of Electronics and Telecommunication, Electrical and Computer Engineering, who want to make their career in VLSI front end design and verification.

**Outcomes of Program:**

1. Design industry grade solutions to real time problems by applying knowledge of digital design
2. Develop front end solution models using Hardware Descriptive Language (HDL)
3. Create verification environment using System Verilog test bench
4. Implement digital systems using Xilinx VIVADO and perform hardware validation on FPGA to meet design specification.

**Number of participants: 24**

**List of Participants:**

Email Address	Name of Student	Class	PRN. No.
2005020@ritindia.edu	Anjali Rajendra Jadhav	T.Y. B. Tech ETC	2005020
2005009@ritindia.edu	Janhavi Mohan Kadam	T.Y. B. Tech ETC	2005009
2005011@ritindia.edu	Shradha Sambhaji Damame	T.Y. B. Tech ETC	2005011
2155001@ritindia.edu	Tejas Bhaskar Jagtap	T.Y. B. Tech ETC	2155001
2005016@ritindia.edu	Vaishnavi Vikas Patil	T.Y. B. Tech ETC	2005016
2005004@ritindia.edu	Shravani Pramod Chavan	T.Y. B. Tech ETC	2005004
2005027@ritindia.edu	Om Chandrashekhkar Khulape	T.Y. B. Tech ETC	2005027
2005032@ritindia.edu	Tanmay Sunil Kamble	T.Y. B. Tech ETC	2005032
surajkitture93@gmail.com	Suraj Balu Kitture	T.Y. B. Tech ETC	2005006
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### **Contents:**

Workshop aims to explore the various aspects of Introduction to VLSI Design. It will focus on the HDL programming language. Detailed content: Hardware Modeling Overview, HDL language concepts, Modules and Ports, Dataflow Modeling, Introduction to Test benches, Operators, Procedural Statements, Controlled Operation Statements, Coding for Finite State Machines, Tasks and Functions

**Two week Internship  
from  
8<sup>th</sup> June 2023**

**VLSI Front end Design**

organized by  
K. E. Society's  
Rajarambapu Institute of Technology,  
Rajaramnagar  
Department of Electronics &  
Telecommunication Engineering



**Resource Person : Mr. Sachin Magdum,  
Sourabh Patil, Abhishek Borote,  
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Asst.Prof. RIT Rajaramnagar**



**About Institute**

Rajarambapu Institute of Technology, Rajaramnagar (formerly known as College of Engineering, Sakharale) was started in 1983. RIT has emerged as a leading technological institute in western Maharashtra through its dedicated and disciplined approach to provide quality technical education over a period of more than 37 years.

**About Department**

Electronics and Telecommunication Engineering department of Rajarambapu Institute of Technology, established in the year 1991 with an undergraduate program in Electronics Engineering is NBA accredited five times in years 2003, 2007, 2013, 2016 and 2019. Department also runs M. Tech. Program Electronics Engineering accredited thrice by NBA, New Delhi. The department is recognized as Ph.D. research center by Shivaji University, Kolhapur. Department has well qualified and experienced faculty and state of art laboratories. It follows Outcome-based teaching learning process. Department has developed several commercial products which are already available in market.

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**Objectives**

- To develop interest among students for VLSI design.
- To explore various aspects of VLSI design.
- To develop skillsets for HDL programming

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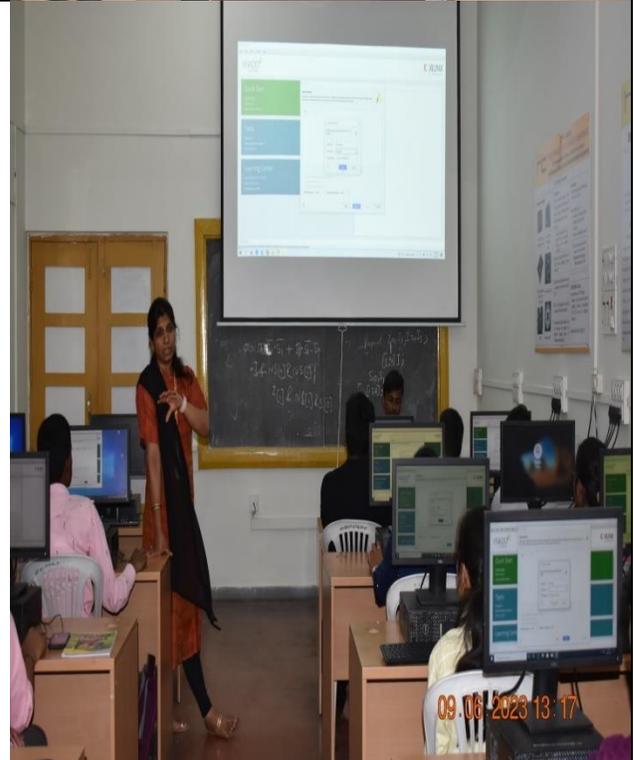
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